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FIG. 1

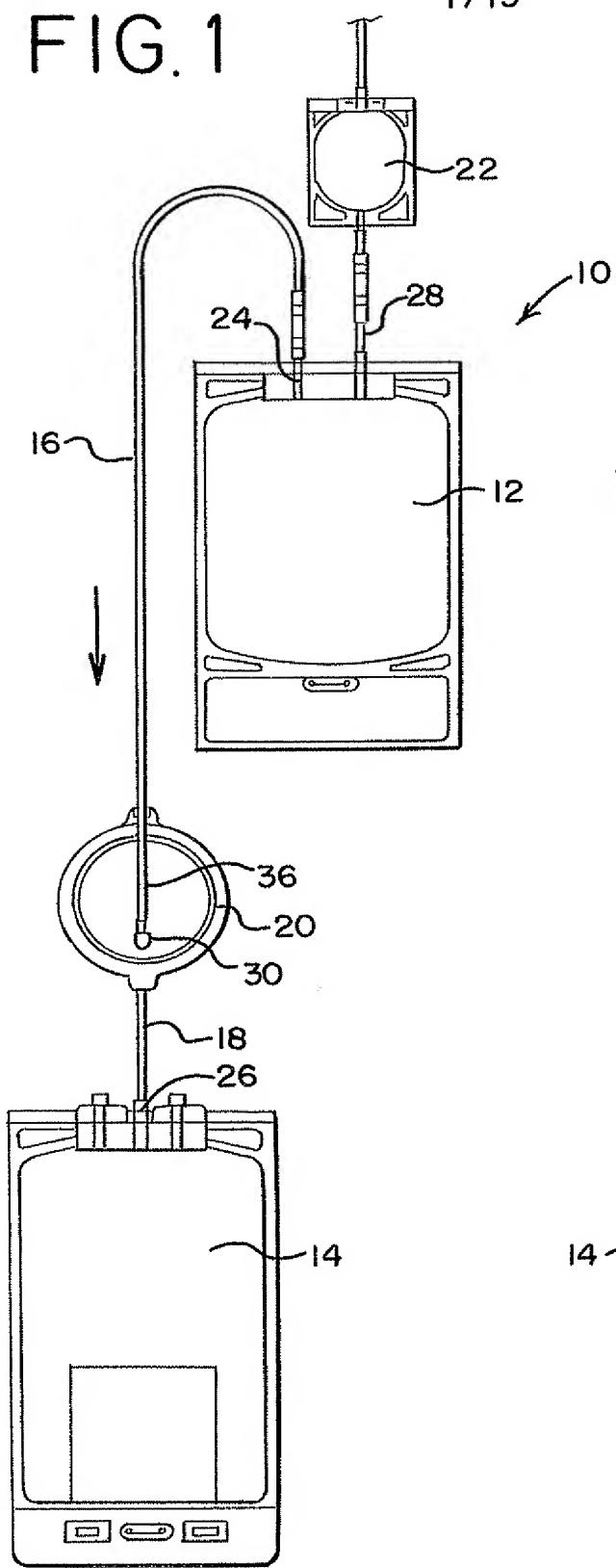
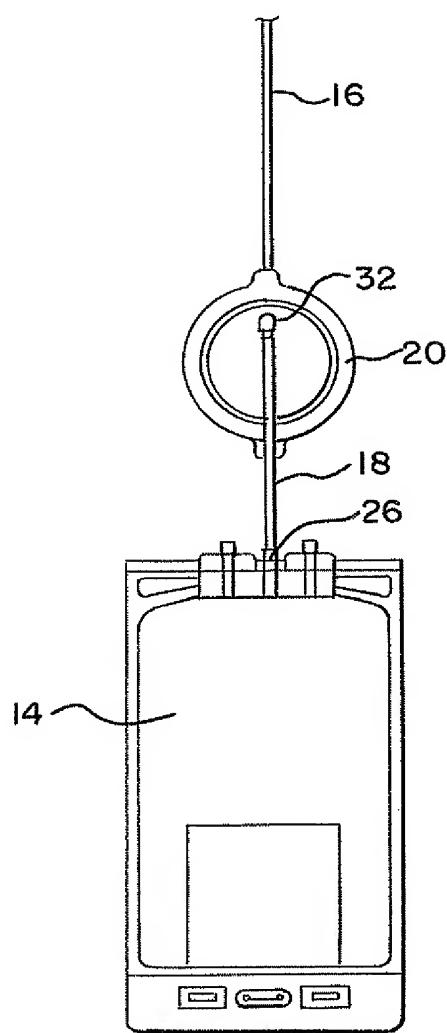


FIG. 2



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FIG. 1A

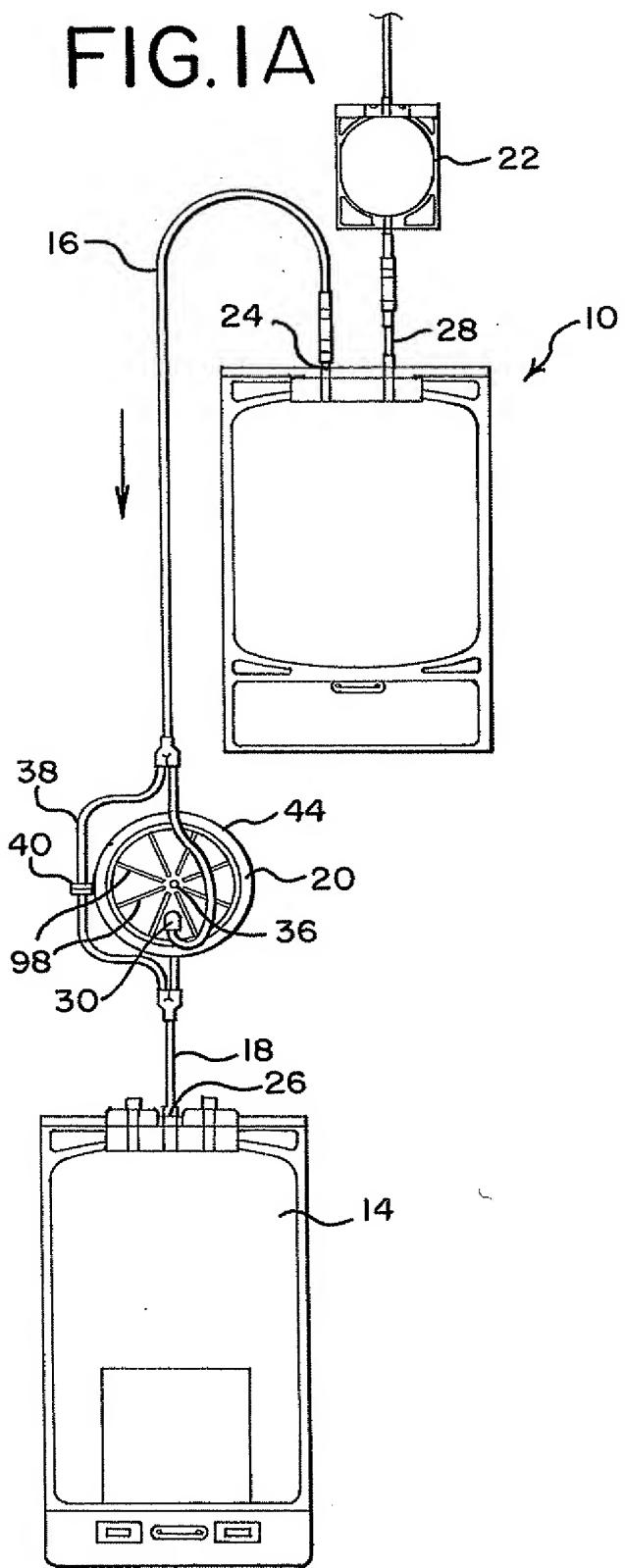
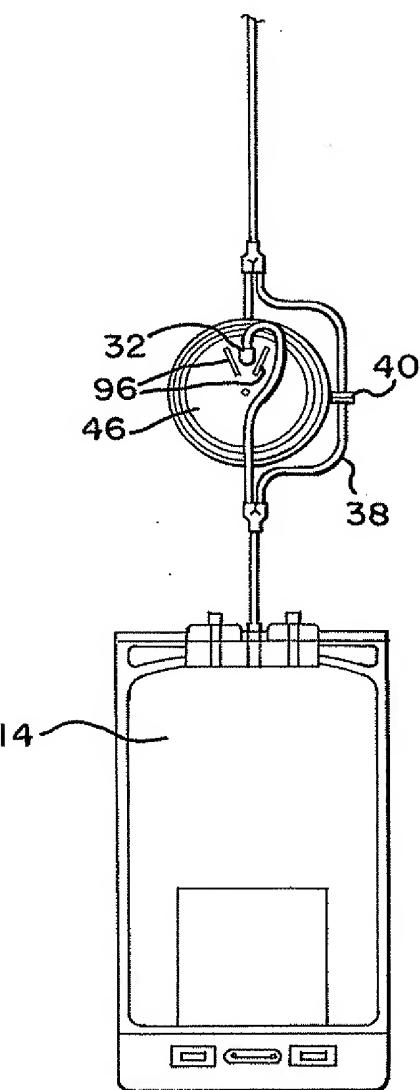
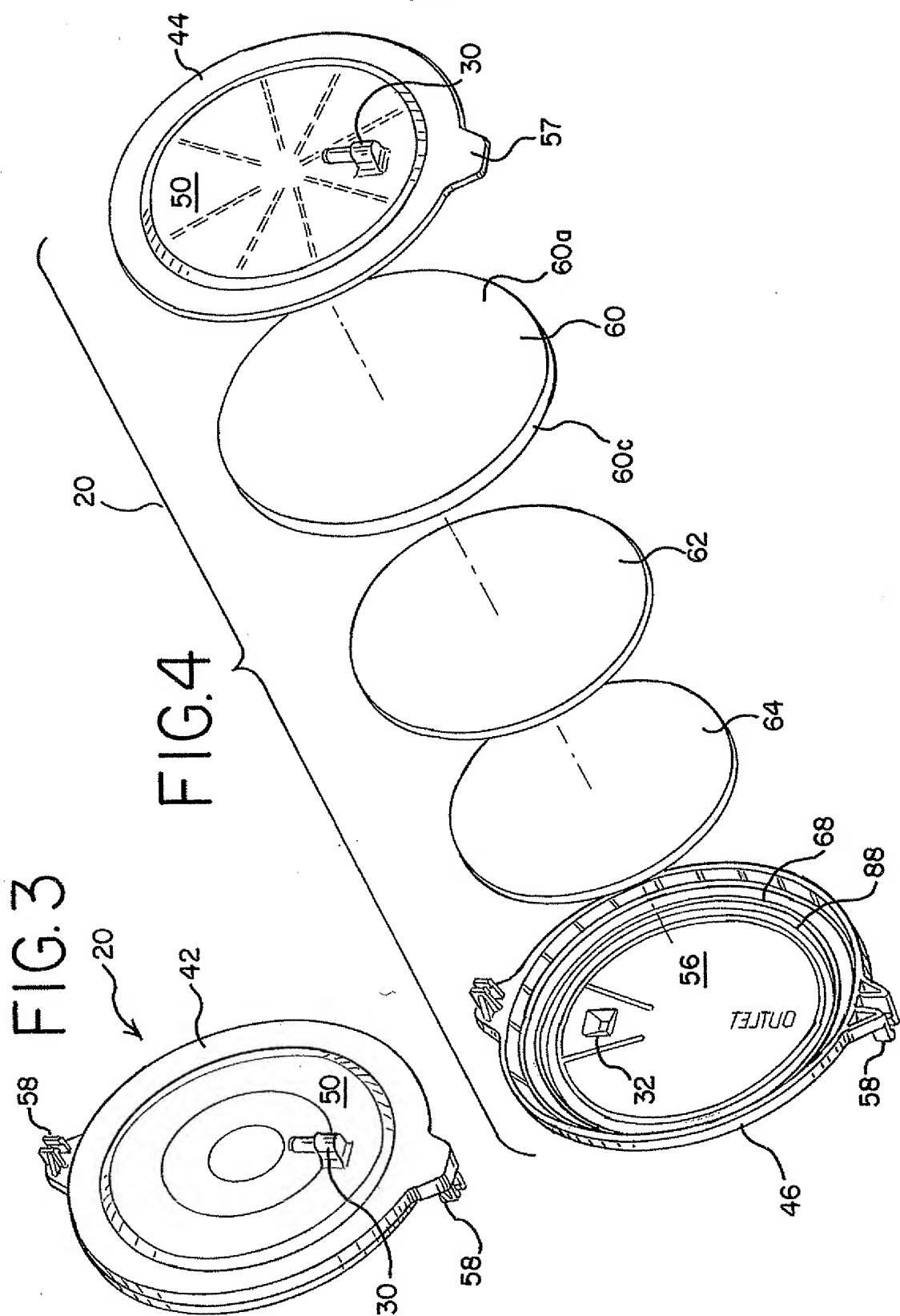


FIG. 2A



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FIG. 6

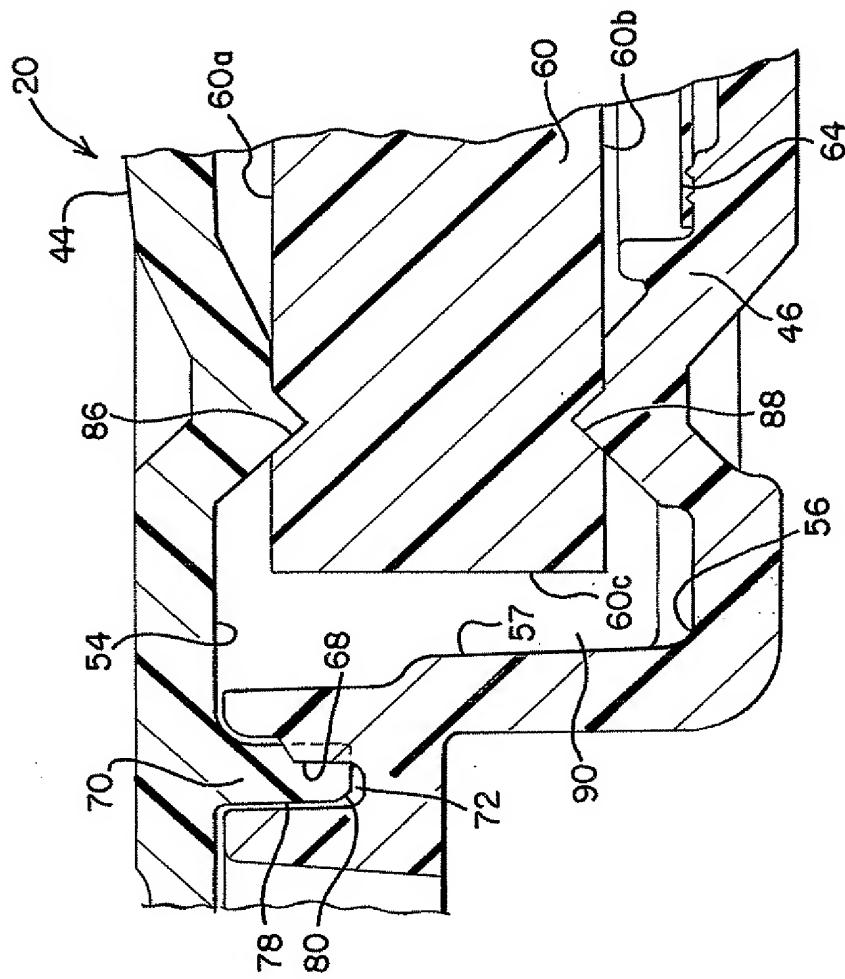
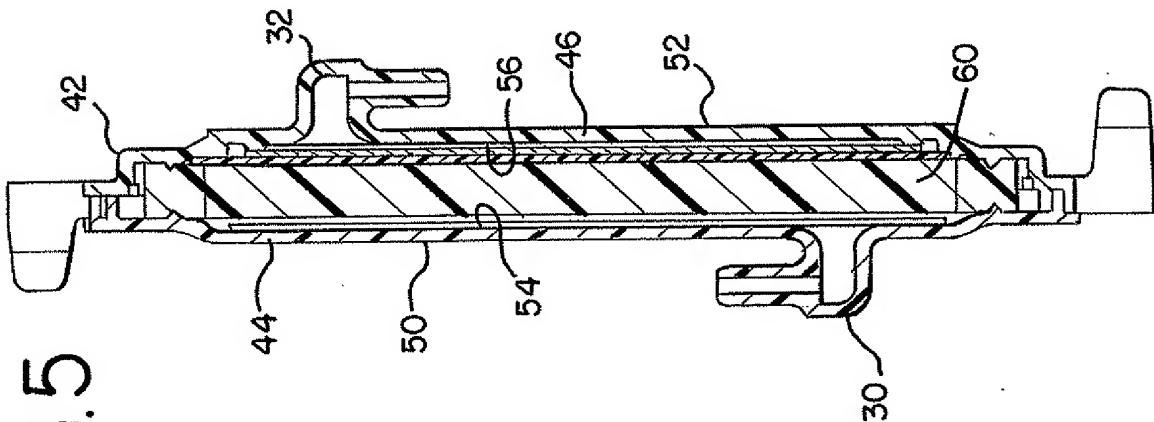


FIG. 5



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FIG.7

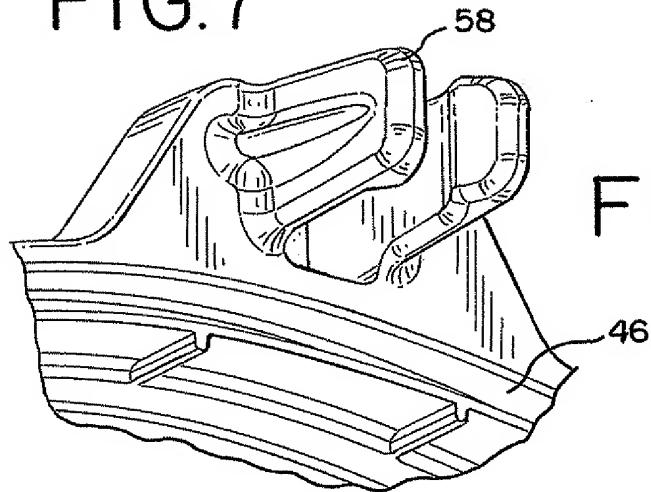


FIG.10

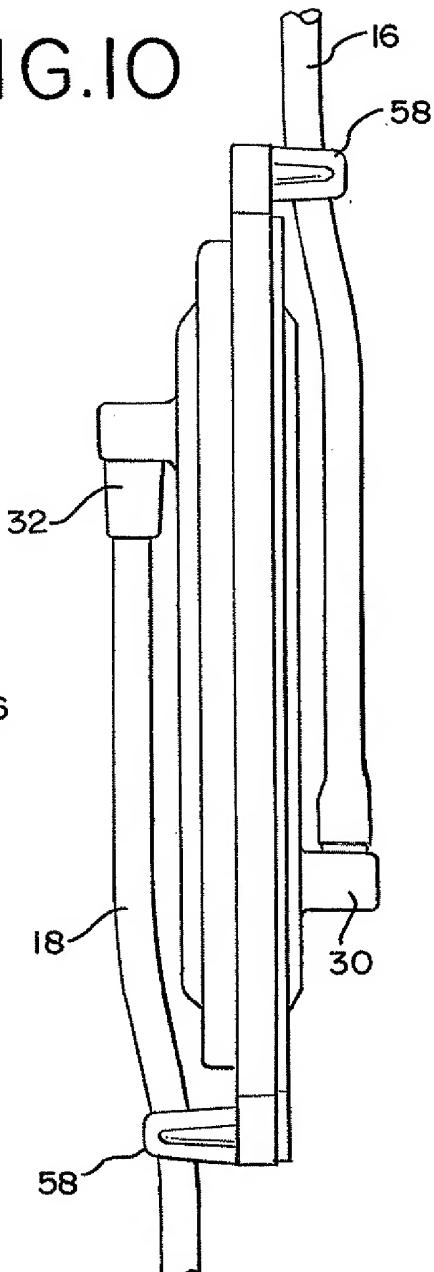


FIG.8

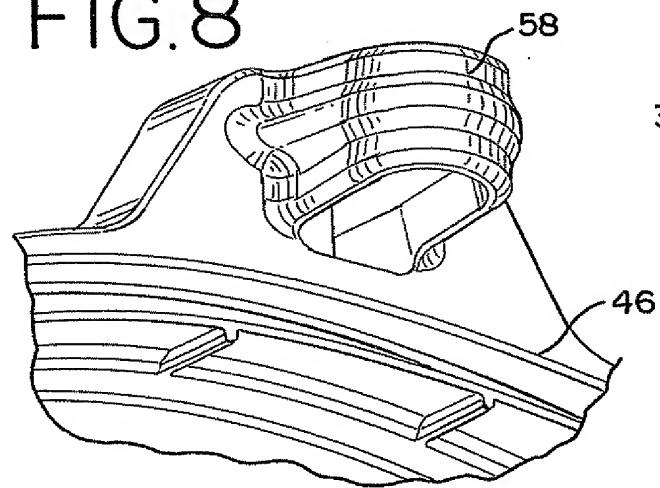
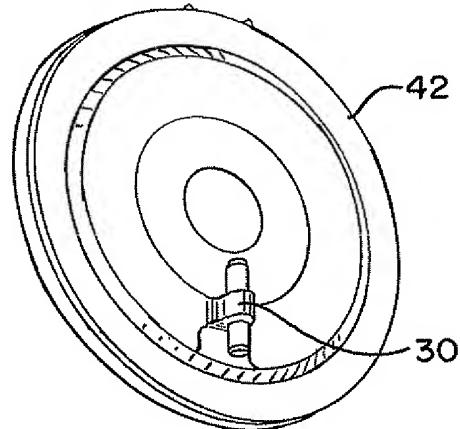


FIG.9



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FIG. 11

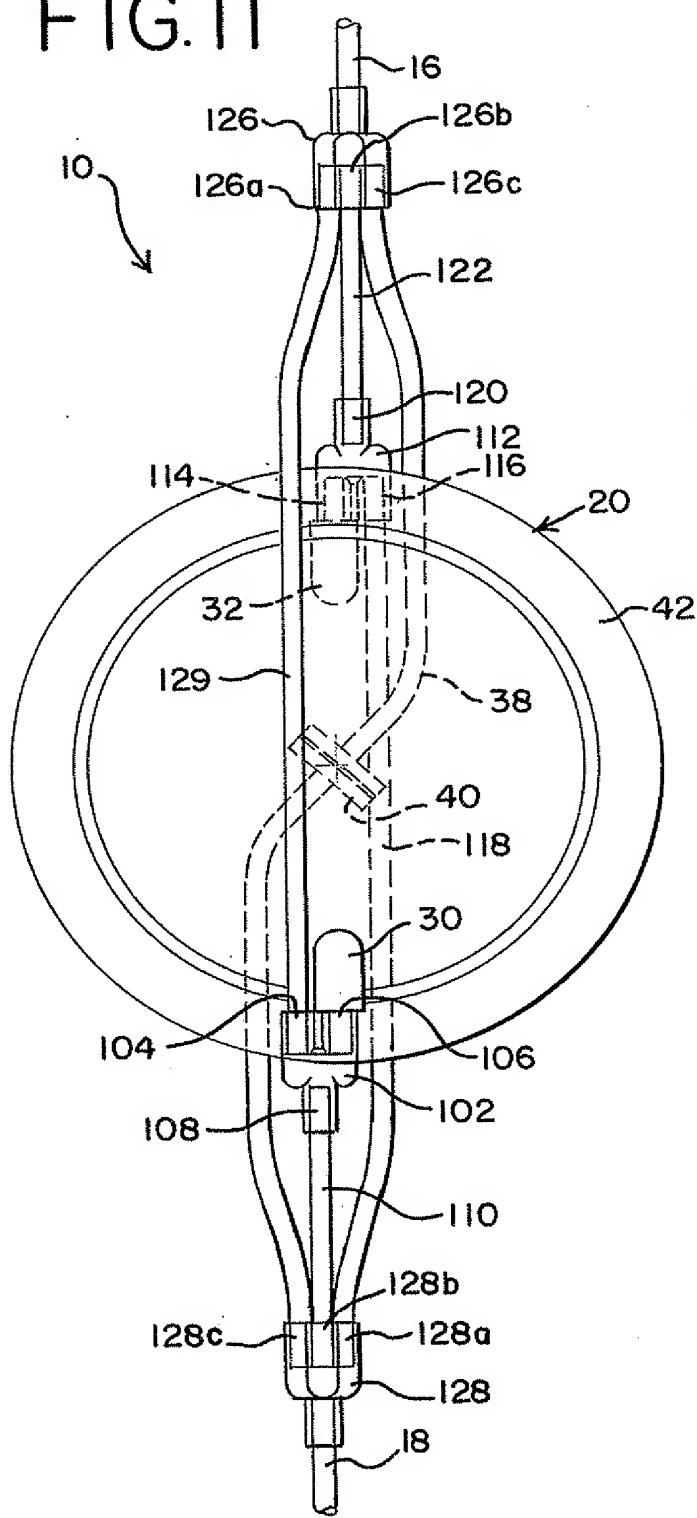
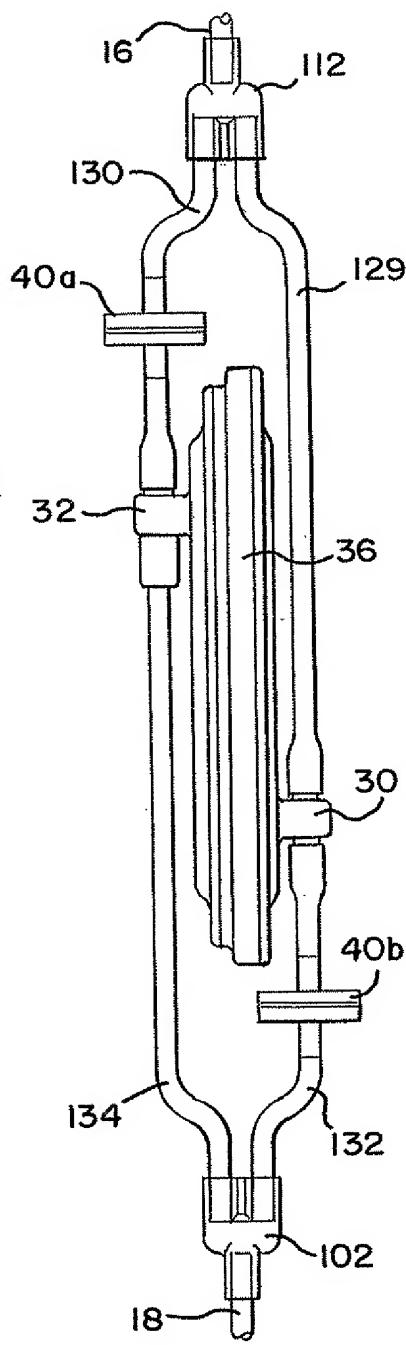


FIG. 12



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FIG. 13

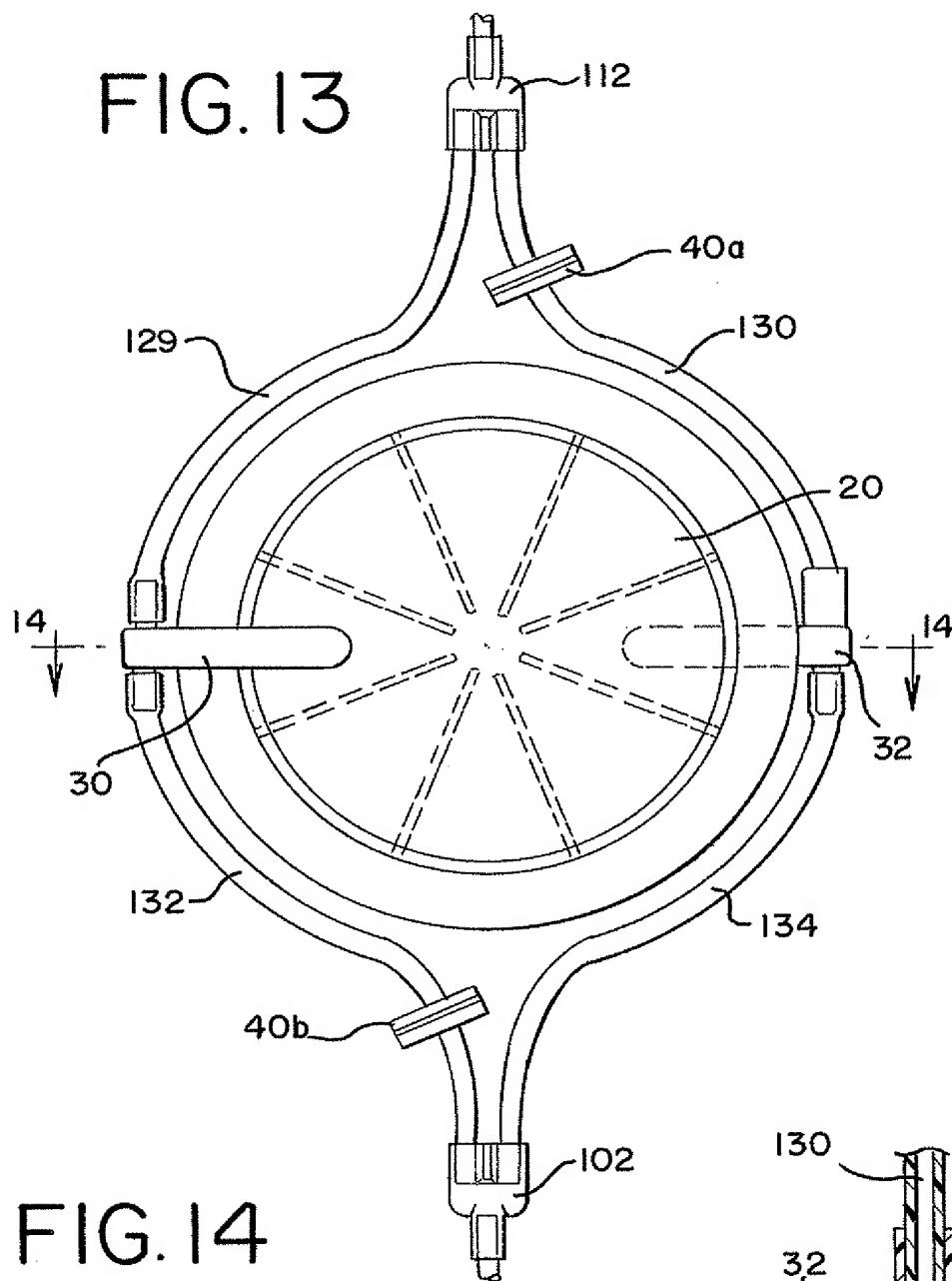
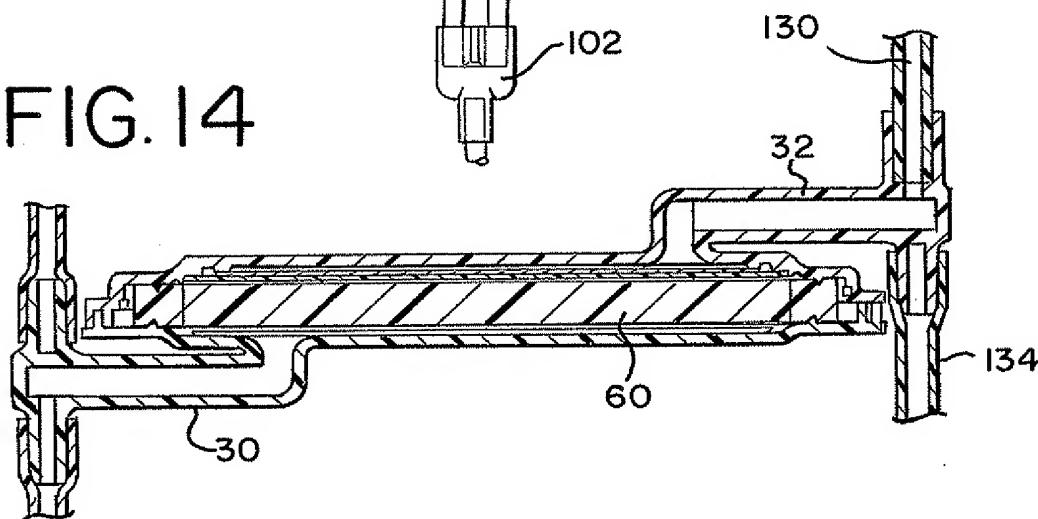
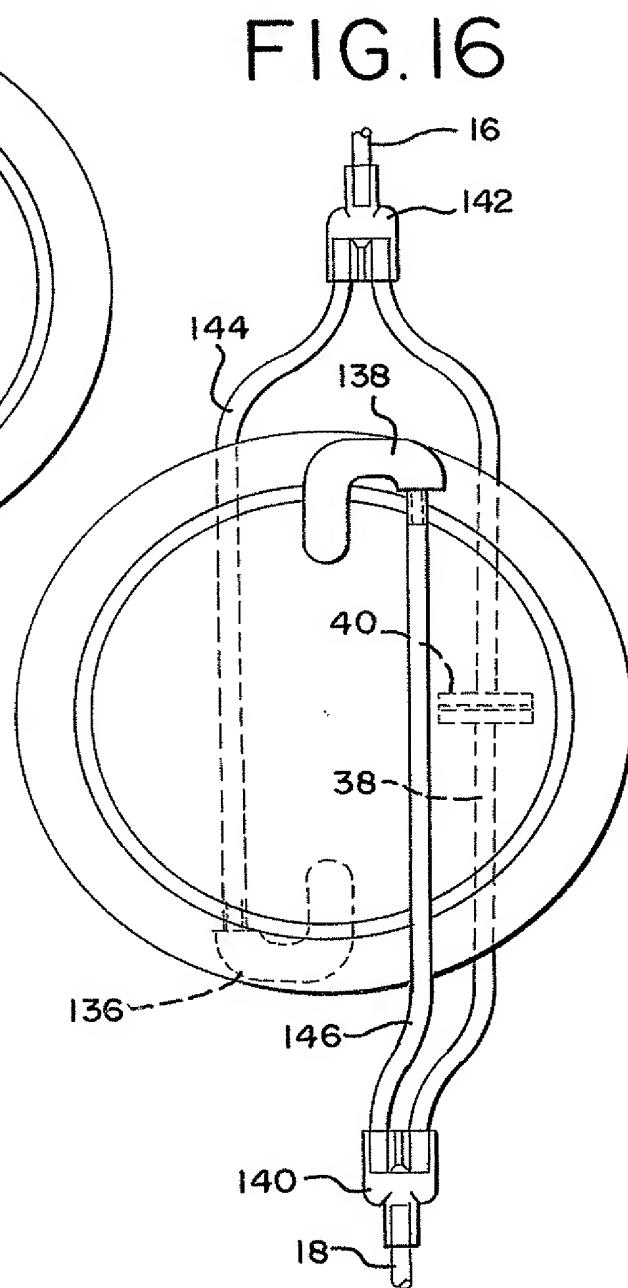
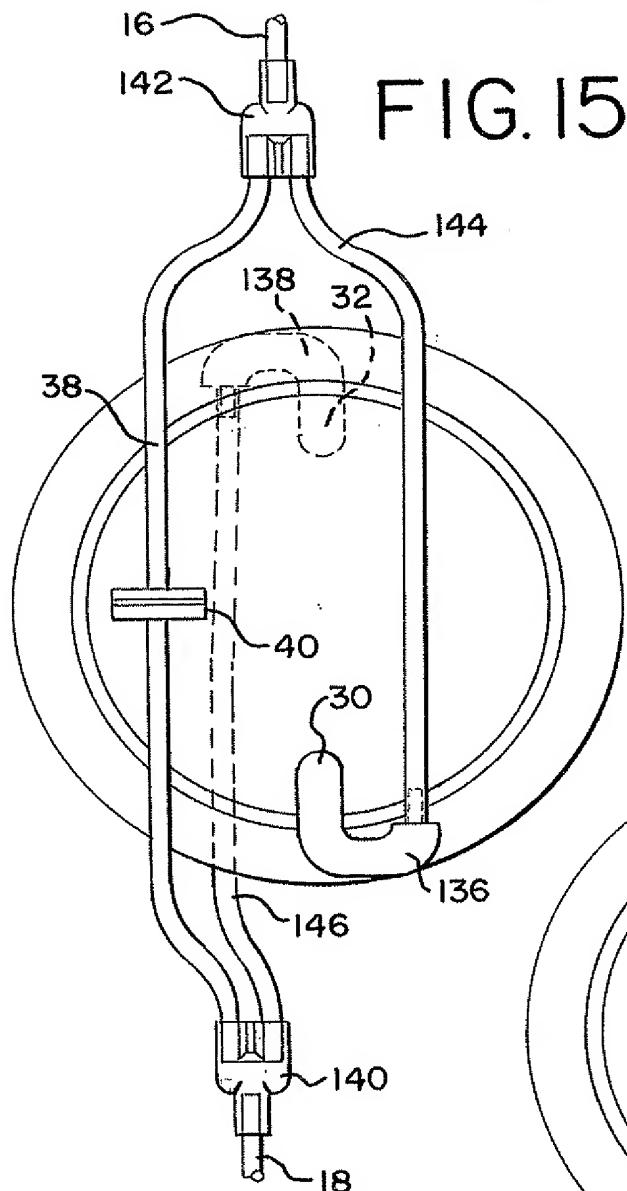


FIG. 14



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FIG. 17

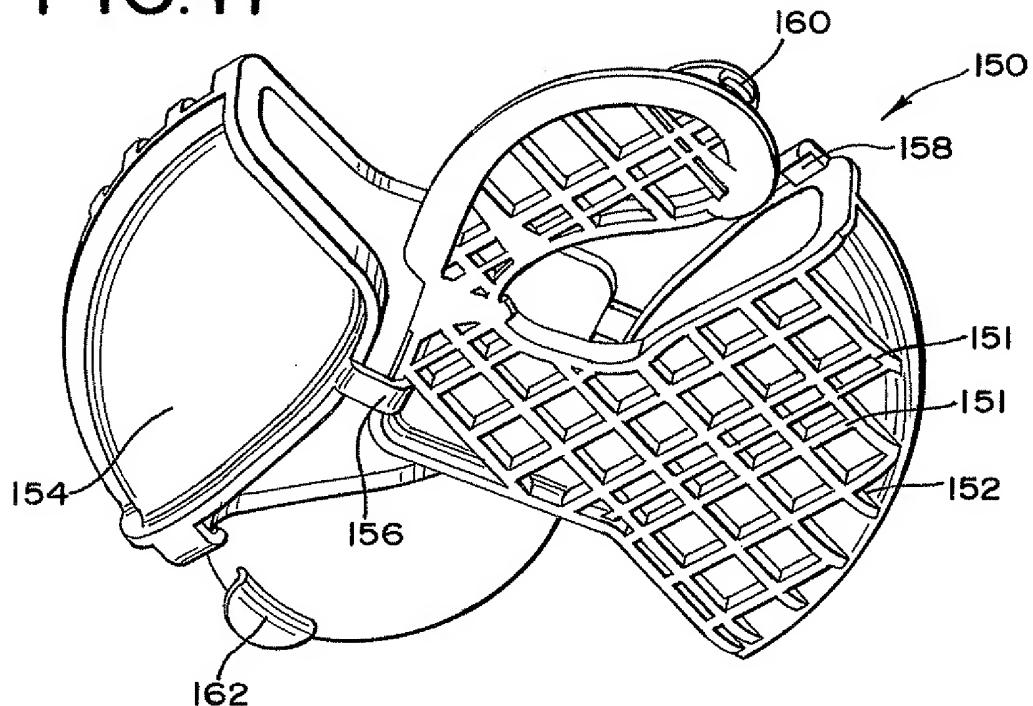
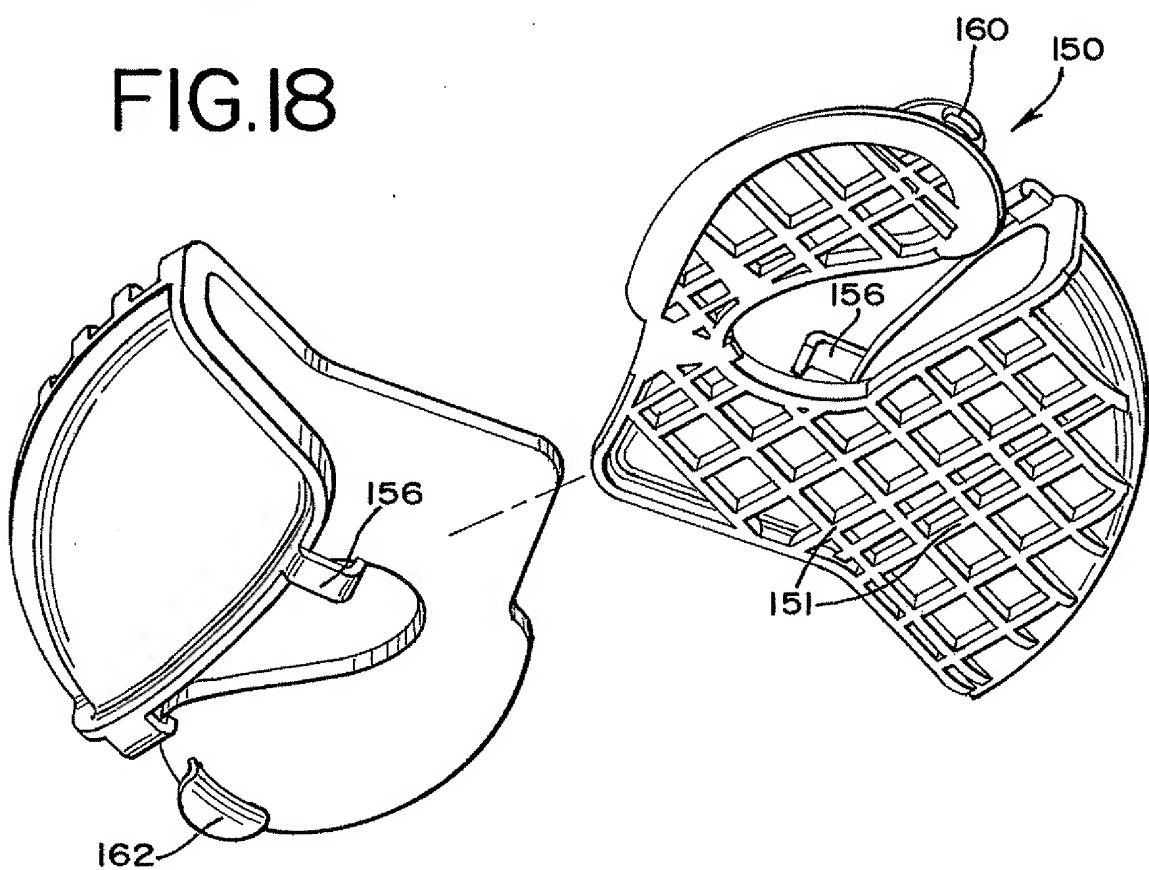


FIG. 18



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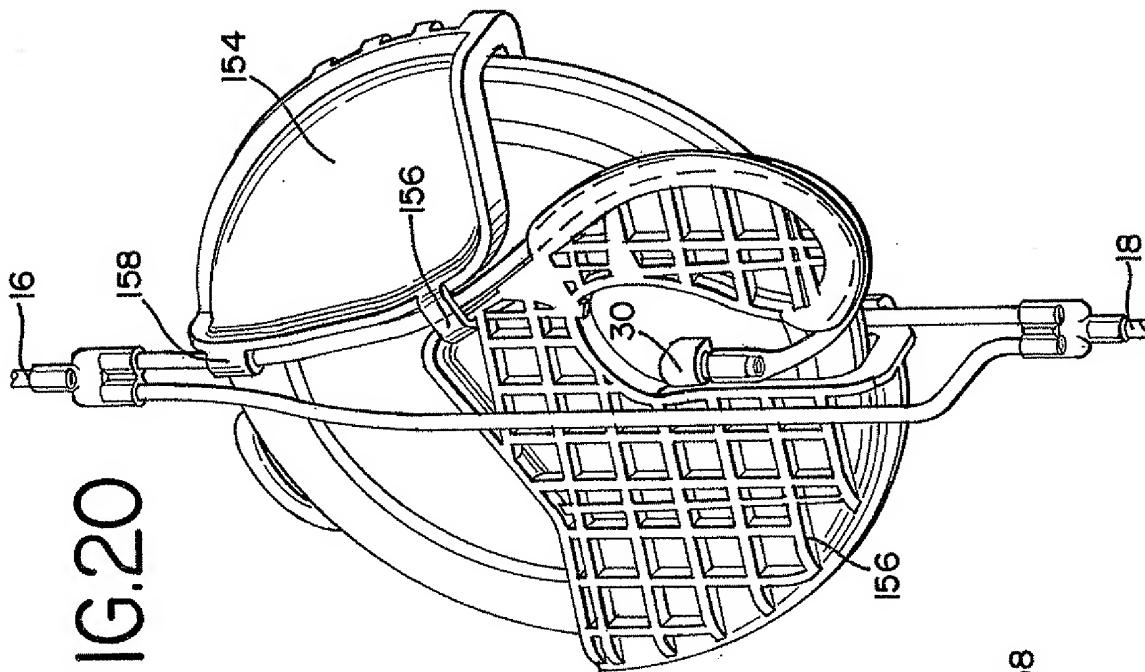


FIG.20

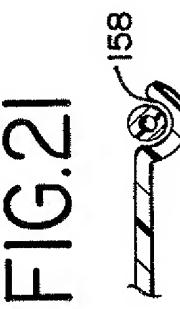


FIG.21

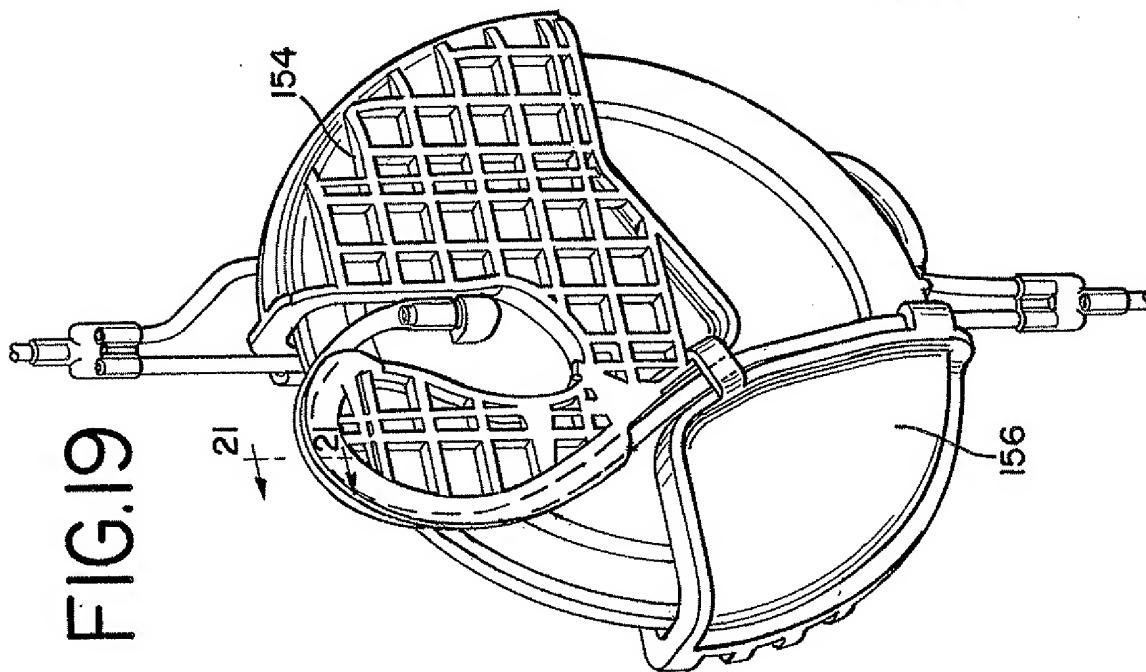


FIG.19

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FIG. 22

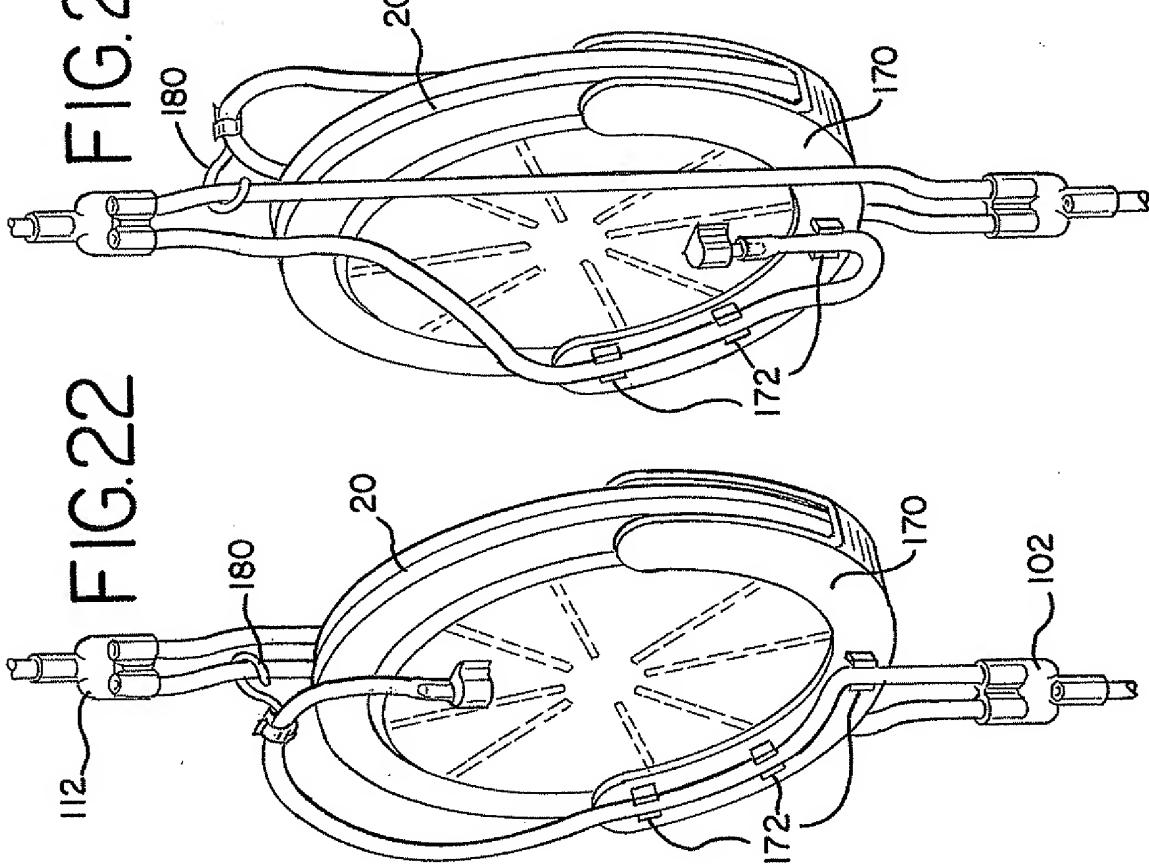


FIG. 23

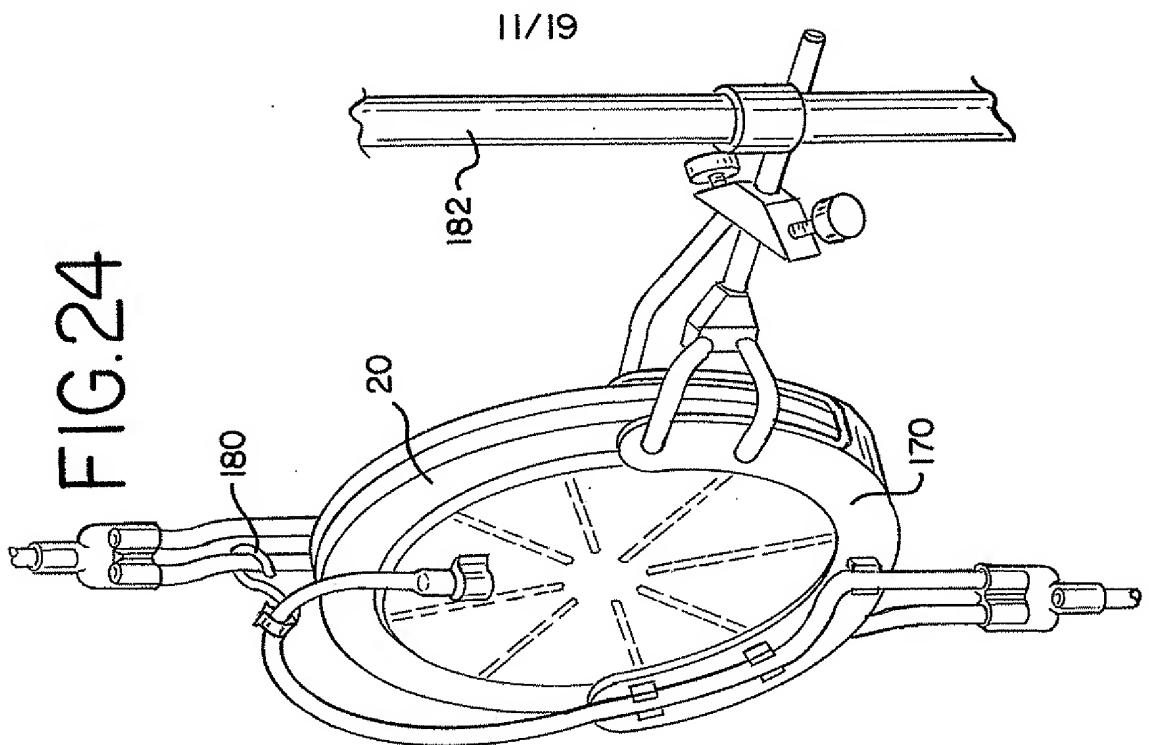
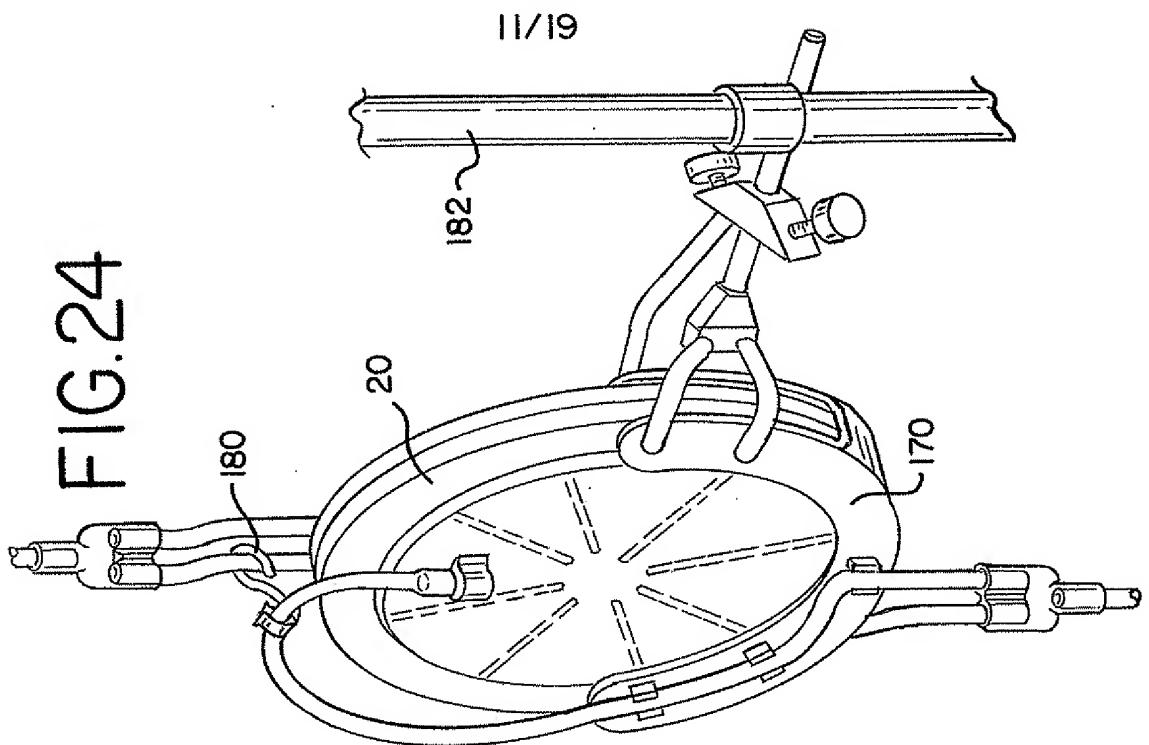


FIG. 24



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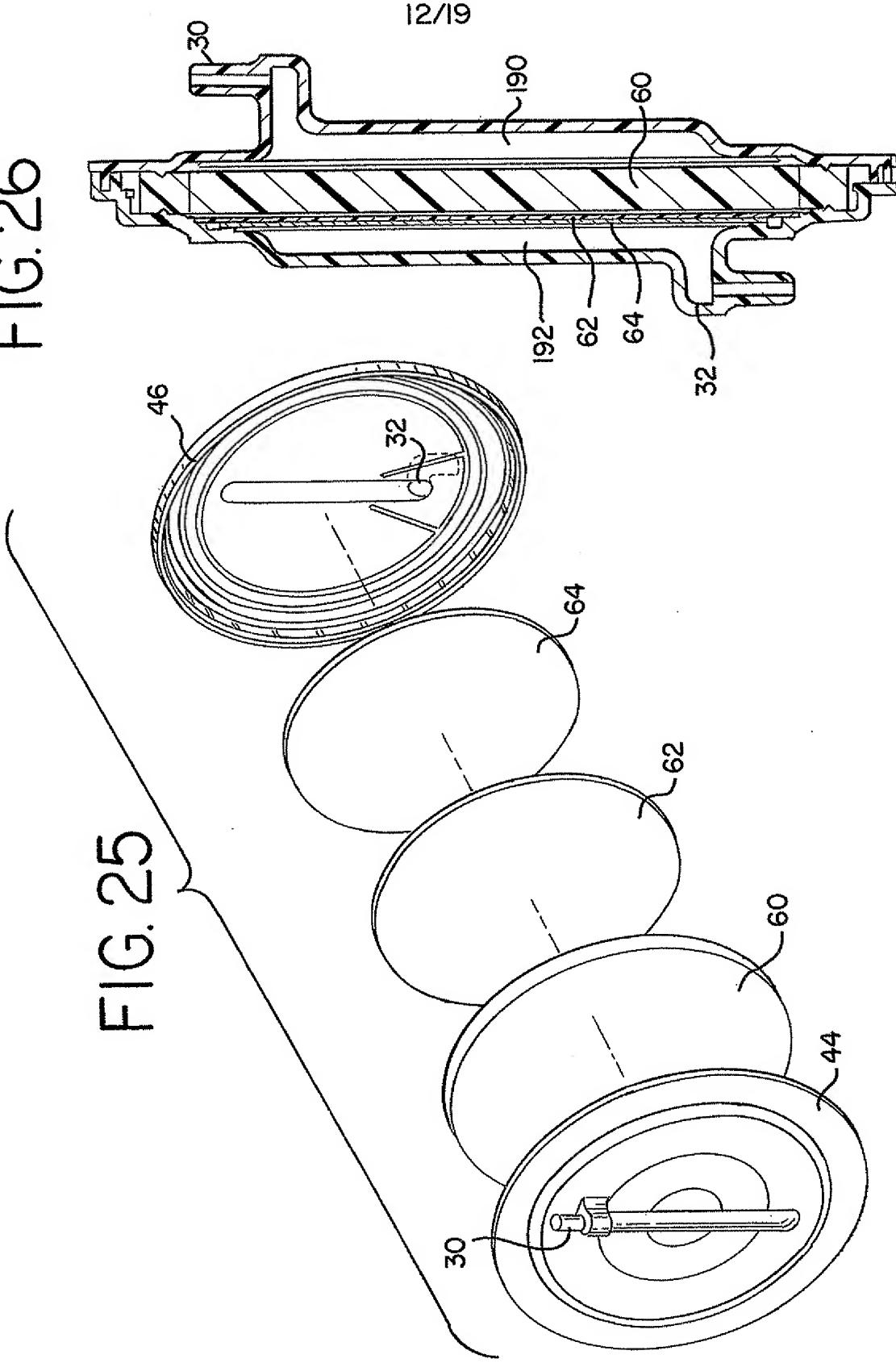
Such Device

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FIG. 26

FIG. 25



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FIG. 27

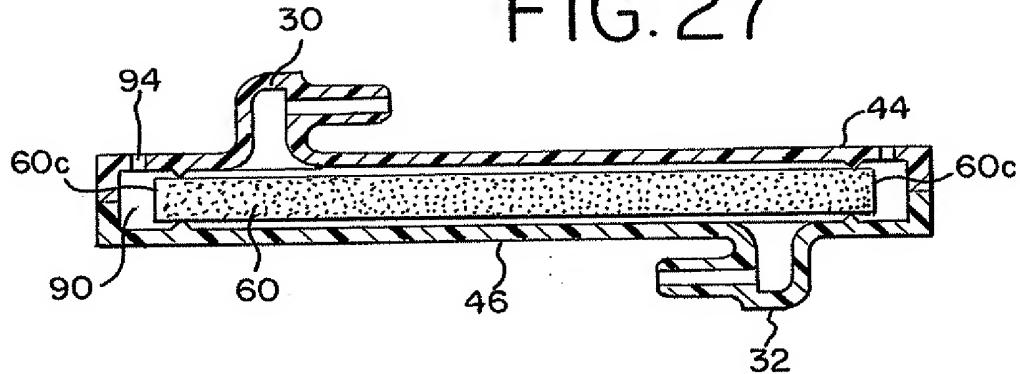


FIG. 28

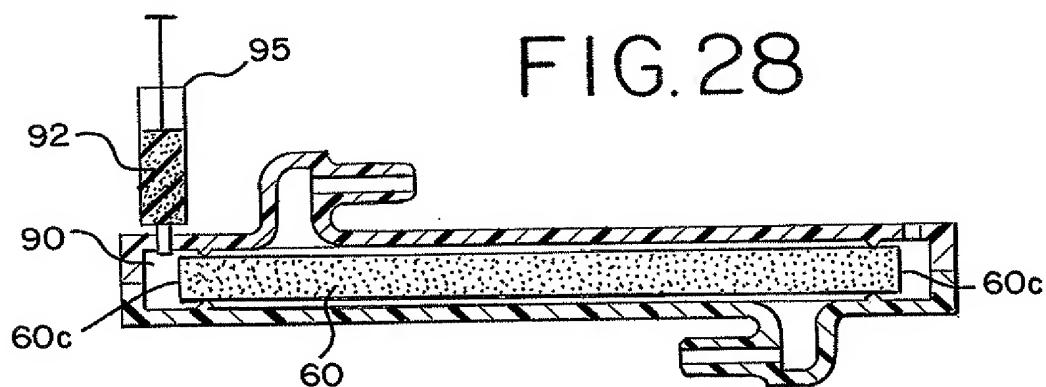
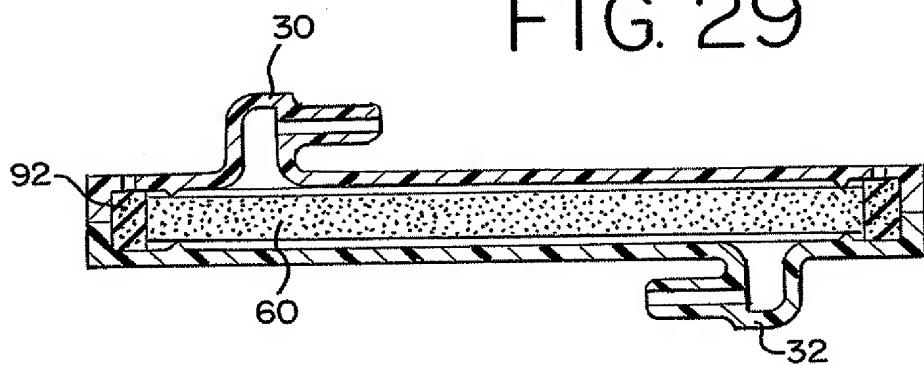


FIG. 29



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FIG.30

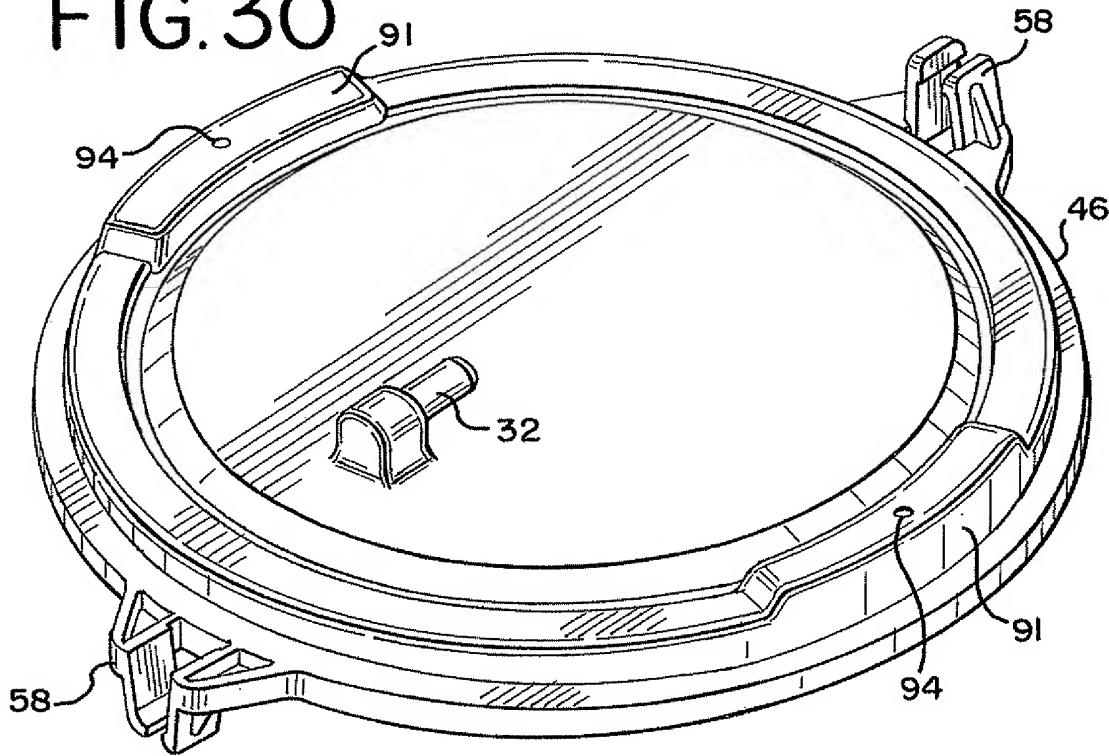
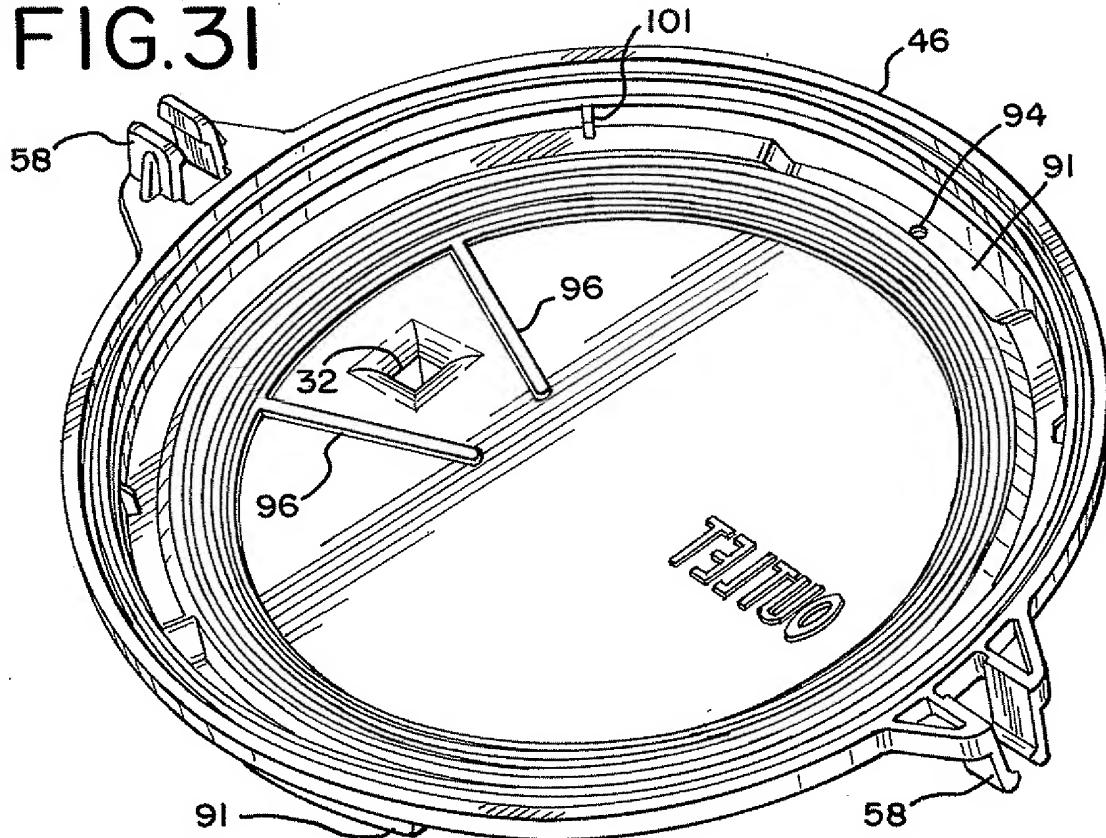


FIG.31



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FIG.32

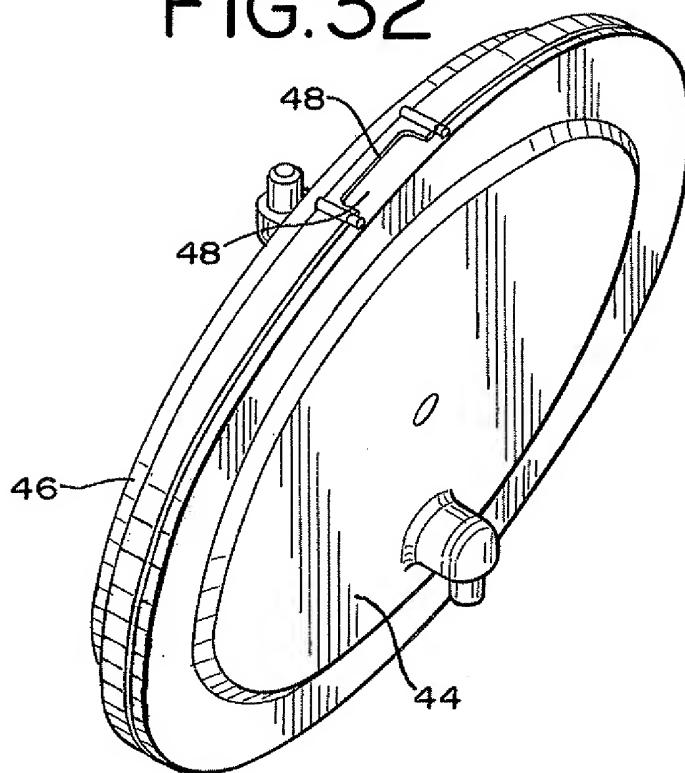


FIG.34

FIG.33

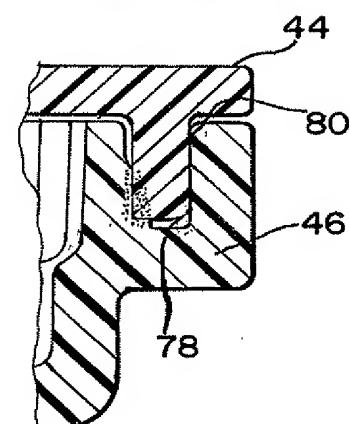
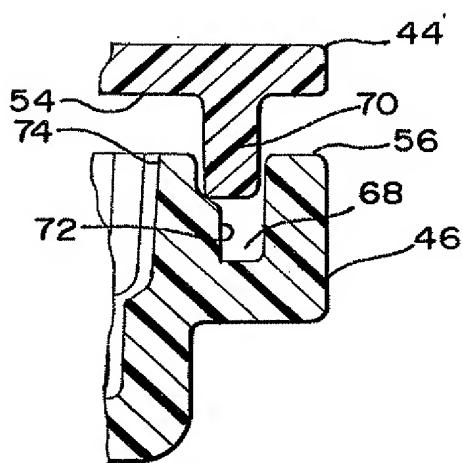


FIG.35

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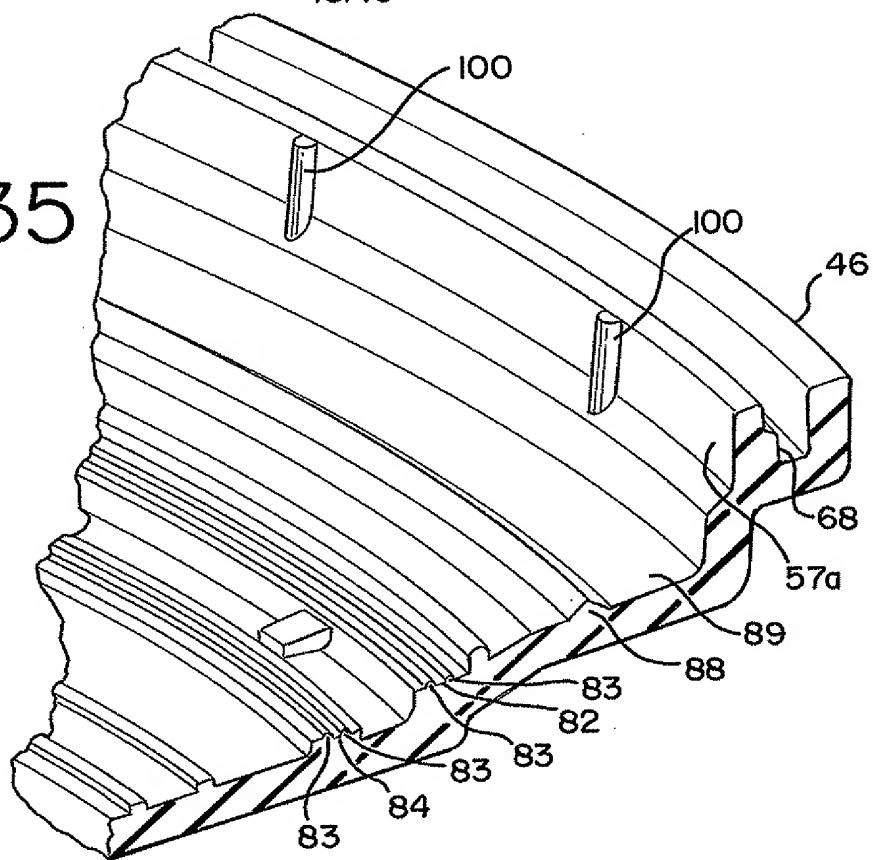
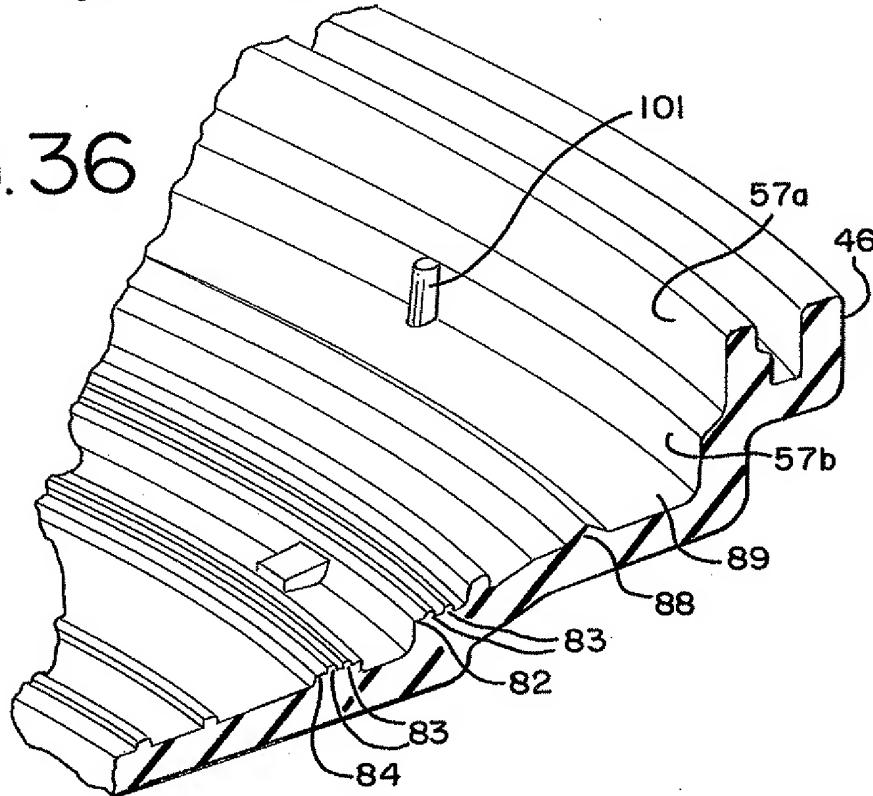


FIG.36



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FIG. 37

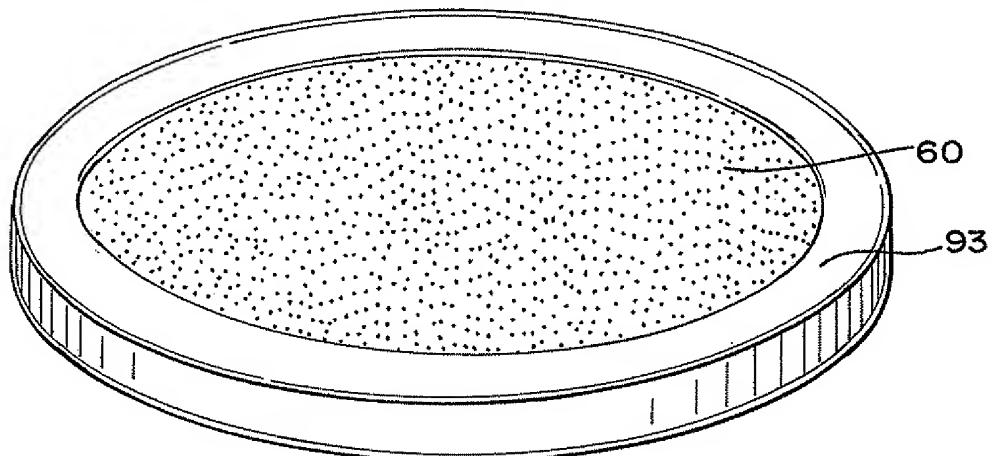


FIG. 38

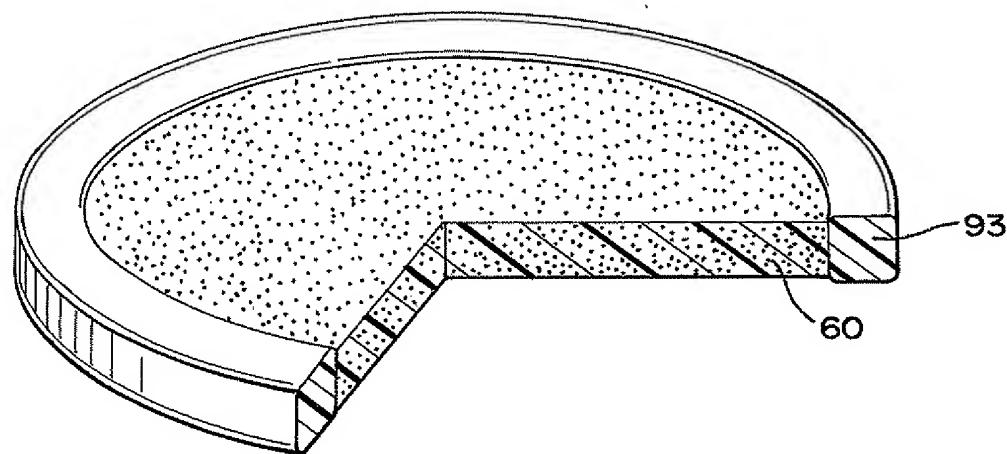
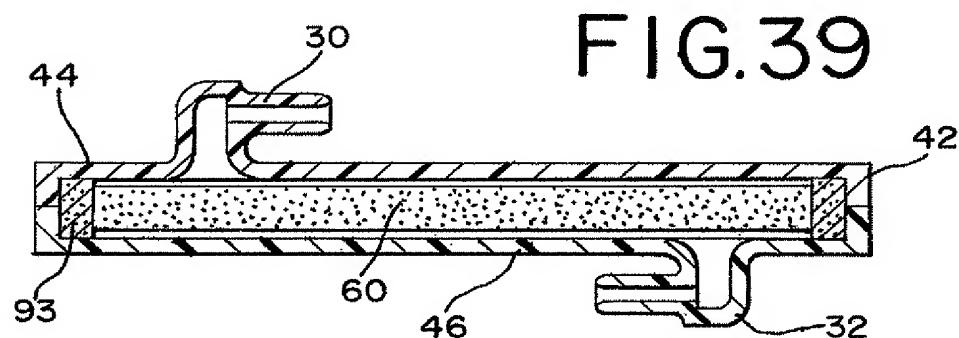


FIG. 39



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FIG. 40

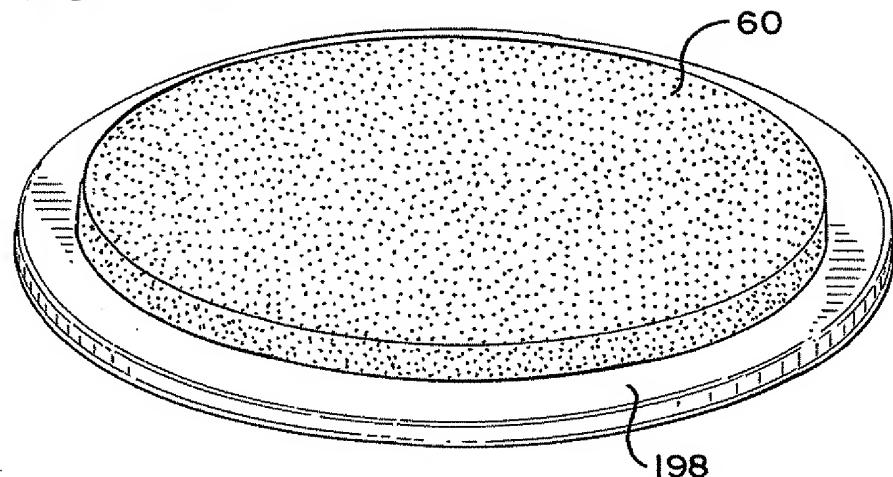


FIG. 41

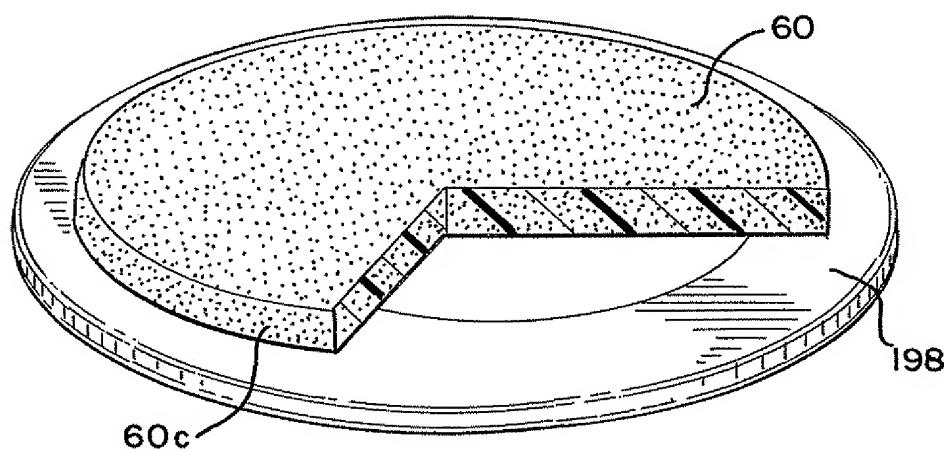
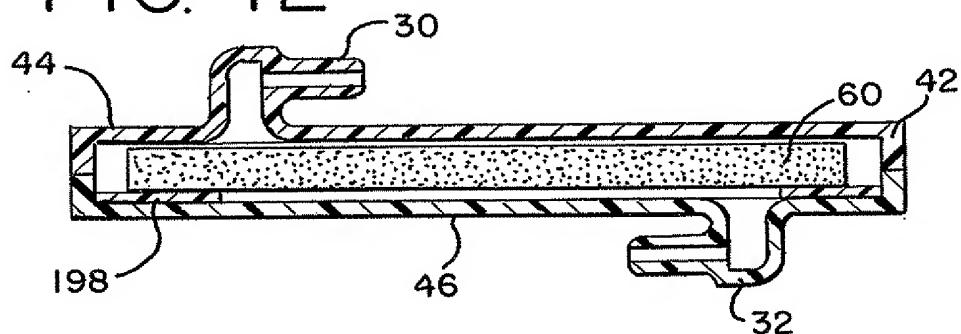


FIG. 42



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FIG. 43

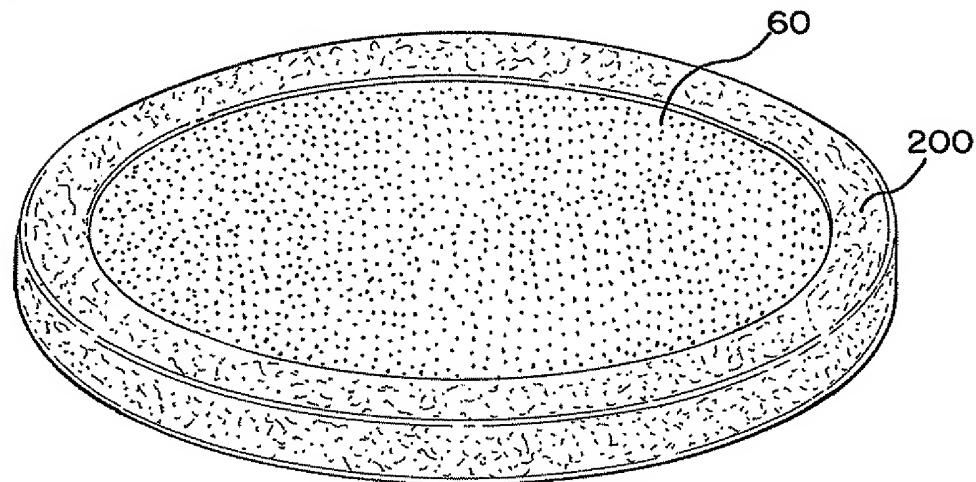


FIG. 44

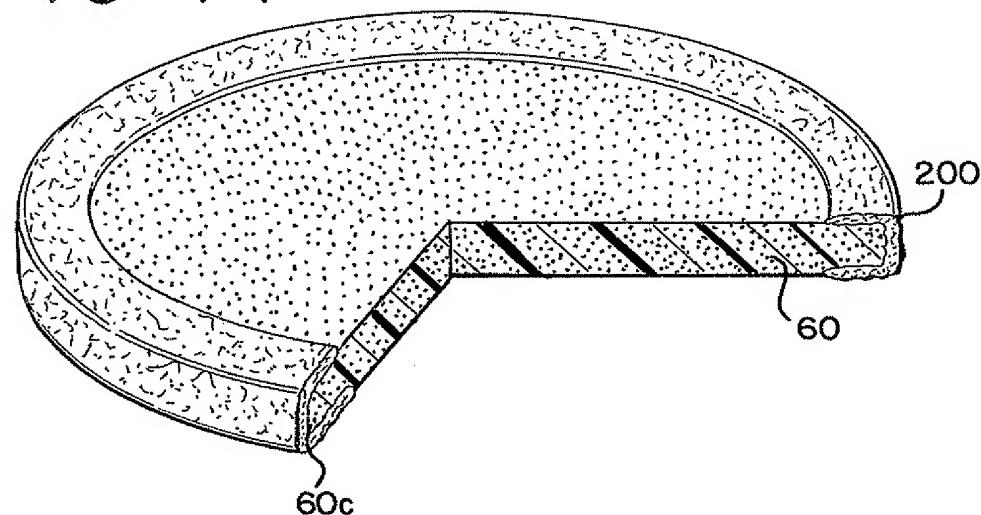


FIG. 45

